



ITW
DAC
PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Art Unit: OIPE

Chen et al.

Examiner: none

Serial No. 10/774,853

Docket No. **SIL-001**

Filed: 2/7/2004

For: **METHOD OF OPTIMIZING IC LOGIC PERFORMANCE BY STATIC
TIMING BASED PARASITIC BUDGETING**

Mail Stop Petition

Commissioner for Patents

P.O. Box 1450

Date 7/11/2005

Alexandria, Va. 22313-1450

Change of Correspondence Address

Dear Sir:

Please change the correspondence address in the above identified patent
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AUG 02 2005

Respectfully submitted,

Ronald Craig Fish

Dated: July 11, 2005

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